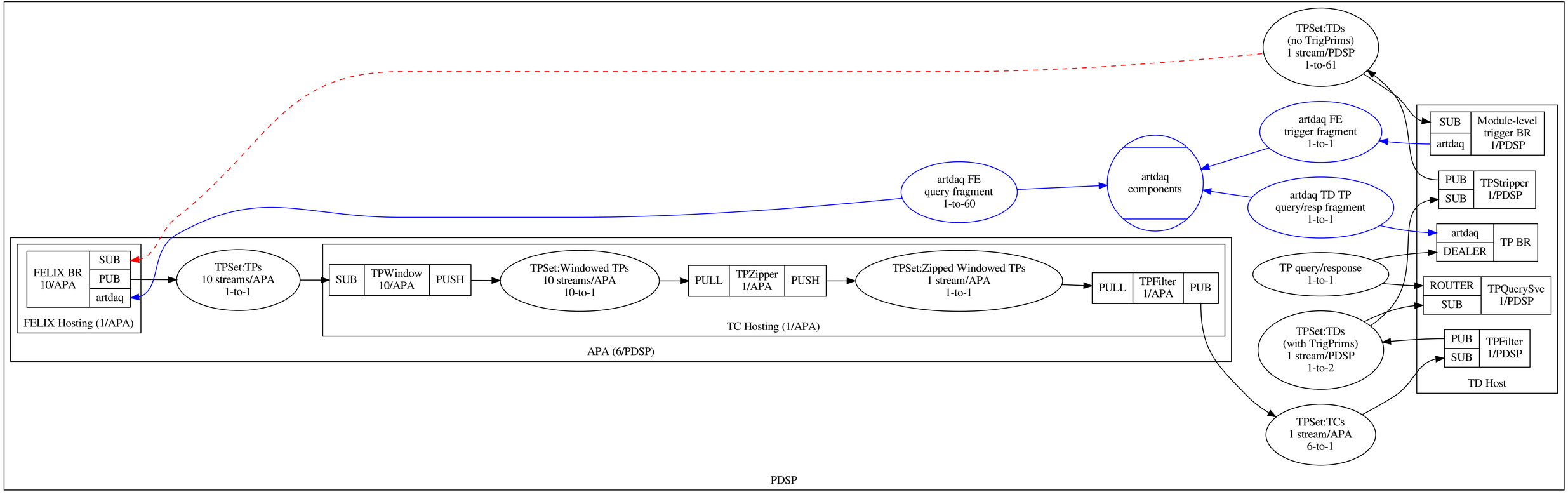


PDSP self-triggering architecture with readback of TD TPs