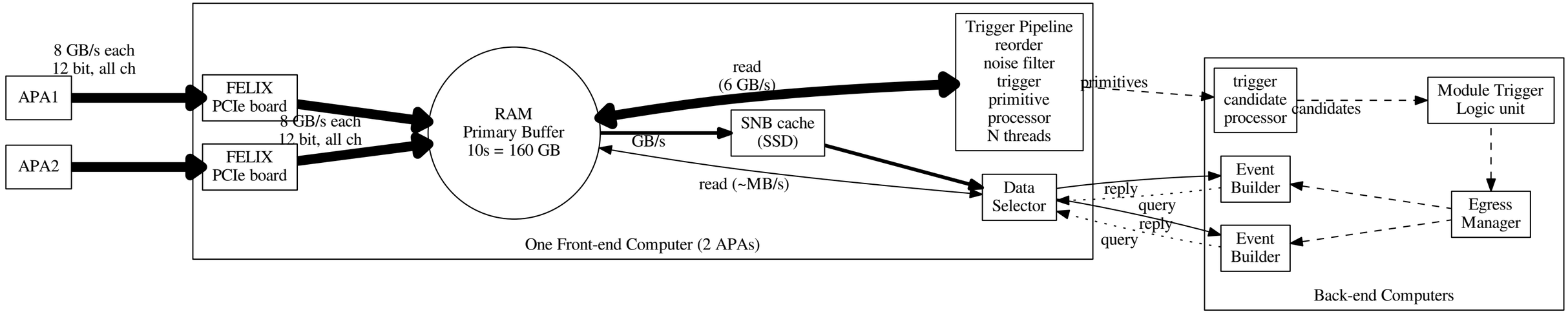

8 GB/s each
12 bit, all ch
APA1
APA2
FELIX
PCIe board
FELIX
PCIe board
8 GB/s each
12 bit, all ch
RAM
Primary Buffer
10s = 160 GB
read
(6 GB/s)
GB/s
read (~MB/s)
SNB cache
(SSD)
Trigger Pipeline
reorder
noise filter
trigger
primitive
processor
N threads
Data
Selector
One Front-end Computer (2 APAs)
primitives
reply
query
reply
query
trigger
candidate
processor
candidates
Module Trigger
Logic unit
Event
Builder
Event
Builder
Egress
Manager
Back-end Computers