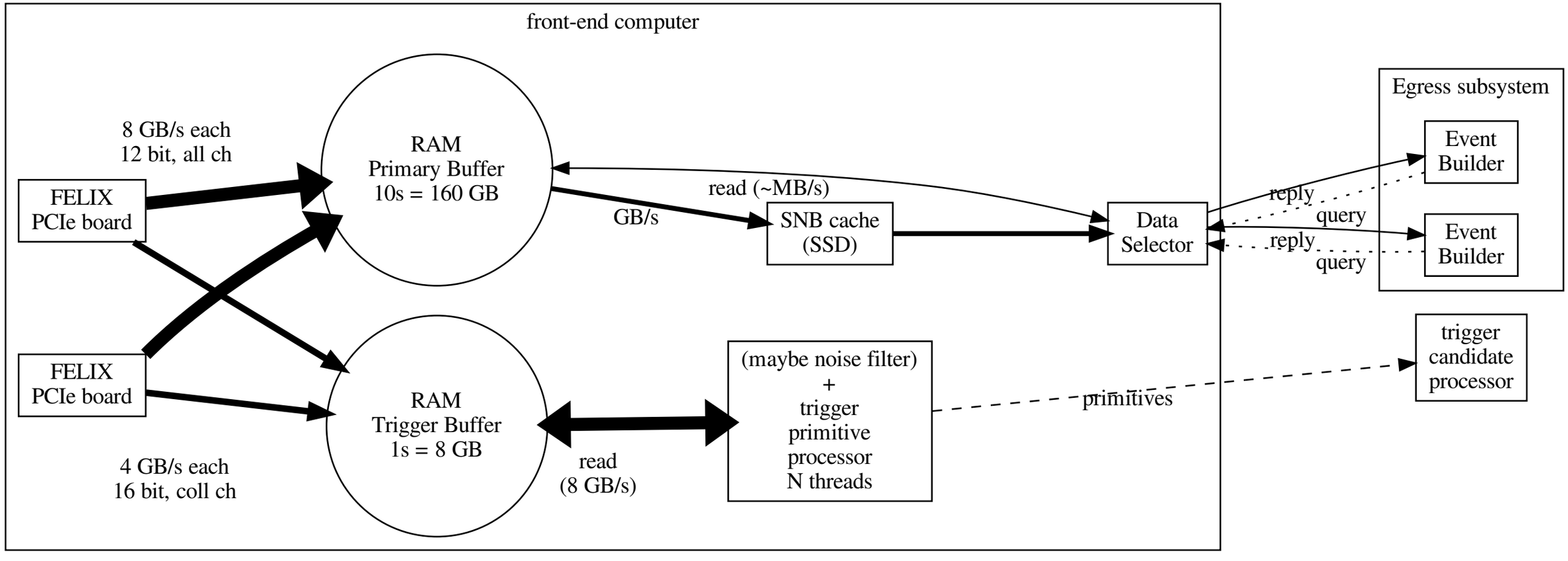

front-end computer
8 GB/s each
12 bit, all ch
FELIX
PCIe board
FELIX
PCIe board
4 GB/s each
16 bit, coll ch
RAM
Primary Buffer
10s = 160 GB
RAM
Trigger Buffer
1s = 8 GB
read (~MB/s)
GB/s
SNB cache
(SSD)
read
(8 GB/s)
(maybe noise filter)
+
trigger
primitive
processor
N threads
Data
Selector
primitives
Egress subsystem
Event
Builder
Event
Builder
reply
query
reply
query
trigger
candidate
processor